

DATA PROCESSING CIRCUITABSTRACT OF THE DISCLOSURE

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A data processing circuit capable of selecting data of a specified channel without using an externally provided circuit for channel selection and outputting the same to a data transfer path, wherein channel identification data is extracted from input data in the application interface circuit based on a packet enable signal input from an MPEG transporter, the extracted channel identification data and channel specifying data are compared. The packet enable signal is output as a packet enable signal to a pre-transmission processing signal when they are identical from the comparison result, while a packet enable signal indicating invalidity is output to the pre-transmission processing circuit when they are not identical.

PCT/JP2007/050001